

# **ECS9926A**

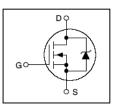
### **Power MOSFET Wafer Datasheet**

#### **FEATURES**

- Die in 8" Wafer Form
- 20V, 6A\*, N-channel
- 100% Tested at Probe

#### **APPLIACTION**

- Battery protection
- Load switch
- Power management



#### Electrical Characteristics of C/P test (T=25°C)

Parameter	Description	Min.	Тур.	Max.	Unit	Test Condition
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	20	_	_	V	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	_	_	19.5	mΩ	V <sub>GS</sub> =4.5V, I <sub>D</sub> =1A **
		_	_	27.5	mΩ	V <sub>GS</sub> =2.5V, I <sub>D</sub> =1A **
V <sub>GS(th)</sub>	Gate Threshold Voltage	0.55	_	0.95	V	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$
I <sub>DSS</sub>	Drain-to-Source Leakage Current	_	_	1	μΑ	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V
I <sub>GSS</sub>	Gate-Body Leakage Current	_	_	±100	nA	V <sub>DS</sub> =0V, V <sub>GS</sub> =±12V
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range	-55℃ to 150℃ Max				

<b>Mechanical Data</b>	1					
Die Size	750 × 1151(dual die)	um <sup>2</sup>	Chip Drawing			
Gate Pad Size			(Scribe Line is Excluded. Unit:µm)			
Scribe Line Size	60	μm	690um			
Wafer Diameter	200	mm	150um			
Wafer Thickness	185	μm				
Flat Position	90	deg	540um			
Source Metallization	Al, Cu		m lum			
Drain Metallization	Ti, Ni, Ag					
			mureur			
Recommended Store in original container, in dry nitrogen,						
Storage Environment	< 6 months at an ambient temperat					

<sup>\*</sup> Values will be influenced by measurement and package.

<sup>\*\*</sup> Pulse Test: Pulse width ≤300µs, Duty cycle ≤ 2%.



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